

10031526

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**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

<b>APPL NUM</b> 10031526	<b>FILING DATE</b> 05/09/2002	<b>CLASS</b> 257	<b>SUBCLASS</b> 119	<b>GAU</b> 2811	<b>EXAMINER</b> T. Thomas <i>Loke</i>
<b>**APPLICANTS:</b> Plikat Robert; Feiler Wolfgang;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/DE00/02061 06/24/2000 <i>Loke</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> GERMANY 199 33 969.4 07/20/1999 <i>Loke</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>Loke</i>				<b>ATTORNEY DOCKET NO</b> 10191/2231	
<b>TITLE :</b> Bi-directional semiconductor component					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner Primary Examiner	
<b>ISSUE FEE</b> Amount Due      Date Paid		<b>DRAWING</b> Sheets Drwg.      Figs. Drwg.      Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b> Application Examiner	
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